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APPLICANTS Atsushi Ikeda, Shiga, JAPAN; Hideo Nakagwa, Shiga, JAPAN; Nobuo Aoi, Hyogo, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP05/09265 05/20/2005 <i>Verified FYL</i>				
** FOREIGN APPLICATIONS ***** JAPAN 2004-165360 06/03/2004 <i>Verified FYL</i>				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 03/08/2007				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met <input type="checkbox"/> Allowance		STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 24
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>FYL</i>		INDEPENDENT CLAIMS 2		
ADDRESS 53080				
TITLE Semiconductor device and method for fabricating the same				
FILING FEE RECEIVED 1100	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	